

<b>INFORMATION DISCLOSURE STATEMENT</b>	Applicants:	Lee, et al.
	Application No.	Not yet assigned
	Filed:	April 12, 2004
	For:	AN ISOLATION-LESS, CONTACT-LESS ARRAY OF NONVOLATILE MEMORY CELLS EACH HAVING A FLOATING GATE FOR STORAGE OF CHARGES, AND METHODS OF MANUFACTURING, AND OPERATING THEREFOR
	Group Art Unit:	Not yet assigned
	Examiner:	Not yet assigned
	Attorney Docket No.:	2102397-993000

Commissioner of Patents and Trademarks P.O. Box 1450 Alexandria, VA 22313-1450		
Dear Sir:		
In accordance with the provisions of 37 C.F.R. § 1.56(a) and 37 C.F.R. § 1.97, Applicant(s) hereby make of record the references listed on the accompanying Form PTO-1449 for consideration by the Examiner in connection with the examination of the above-identified patent application.		
This Information Disclosure Statement:		
(a)	<input checked="" type="checkbox"/>	accompanies a new patent application submitted herewith.
(b)	<input type="checkbox"/>	is filed within three (3) months of the Filing Date or before the mailing date of a First Office Action on the merits; OR
(c)	<input type="checkbox"/>	after the period defined in (b) but before the mailing date of a Final Rejection or Notice of Allowance, OR
(d)	<input type="checkbox"/>	is filed after the first Office Action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned prior to the mailing date of either a final rejection or a notice of allowance, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below OR
(e)	<input type="checkbox"/>	is filed after the mailing date of either a final rejection or a notice of allowance, and the issue fee has not been paid, and is accompanied by the requisite petition fee (\$130) set forth in 37 CFR § 1.17(l)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.
As required under § 1.97(e), Applicants, through the undersigned, hereby state either that [check the appropriate space]:		
(f)	<input type="checkbox"/>	Each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of the Information Disclosure Statement; or

(g)	<input type="checkbox"/>	No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.
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It is respectfully requested that the references shown on the attached Form PTO-1449 be made of record in this application. Copies of the Articles are enclosed.

The Commissioner is authorized to charge any deficiencies and credit any overpayment of fees to our Deposit Account No. 07-1896.

Respectfully submitted,

Date: April 12, 2004

GRAY CARY WARE & FREIDENRICH LLP

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*I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as EXPRESS MAIL (EV 302280553 US) in an envelope addressed to: Commissioner of Patents & Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop – Patent Application.*

April 12, 2004

Date

M. P. Pique  
 Signature

Form PTO-1449 U.S. DEPT. OF COMMERCE Patent and Trademark Office  INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	Atty. Docket No.: 2102397-993000	Serial No.: Not yet assigned
	Applicants: Lee, et al.	
	Filing Date: April 12, 2004	Group Art Unit: Not yet assigned

#### U.S. PATENT DOCUMENTS

Examiners Initials	Patent Number	Date	Name	Class	Subclass	Filing Date
	5,029,130	07-1991	Yeh			
	6,103,573	08-2000	Harari et al			
	6,329,685	12-2001	Lee			
	6,420,231	07-2002	Harari et al			

Examiner:	Date Considered:
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP '609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	

#### FOREIGN PATENT DOCUMENTS

	Document number	Date	Country	Class	Sub-class	Translation	
						YES	NO

#### OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Yoshida, et al., "A 1Gb Multi-Level AG-AND-Type Flash Memory With 10MB/s Programming Throughput for Mass Storage Application," 2003 IEEE International Solid State Circuits Conference, Session 16, 10 pgs., (2003)
	Sasago, et al., "10-MB/s Multi-Level Programming of Gb-Scale Flash Memory Enabled by New AG-AND Cell Technology," IEDM 2002, pp. 952-954.

Examiner:	Date Considered:
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP '609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	